
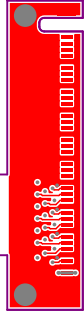
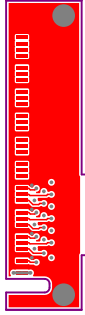
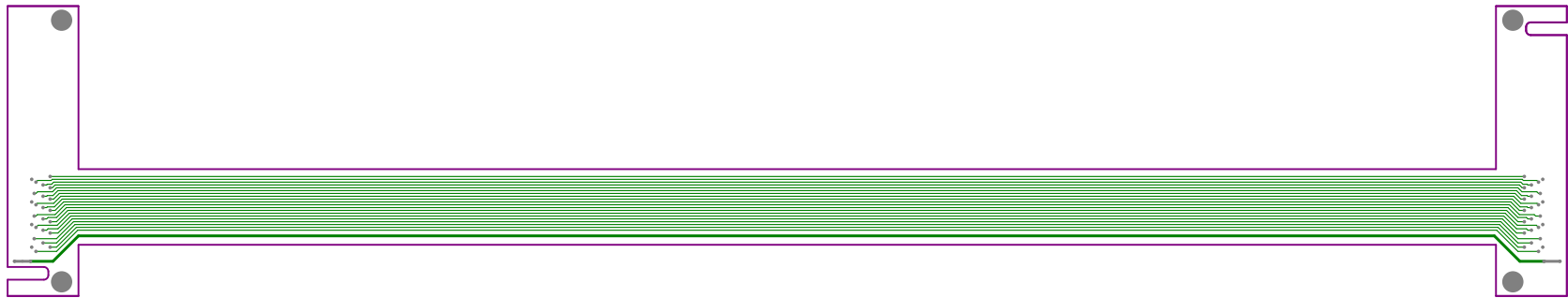
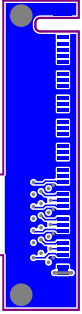
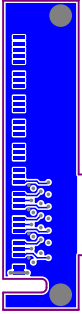


1.0	Release for Production	22/12/2017
Rev	Description	Date
Project/Equipment sTGC Wire Cable for QS1		
<div>Document</div> <div></div>	Designer Alam Toro	
	Drawn by Alam Toro	03/11/2017
	Check by -	-
	Last Mod. -	22/12/2017
	File wCable_QS1.SchDoc	
Print Date 22/12/2017 14:11:59		Sheet 1 of 1
European Organization for Nuclear Research CH-1211 Genève 23 - Switzerland		Size A4 Rev 1.0















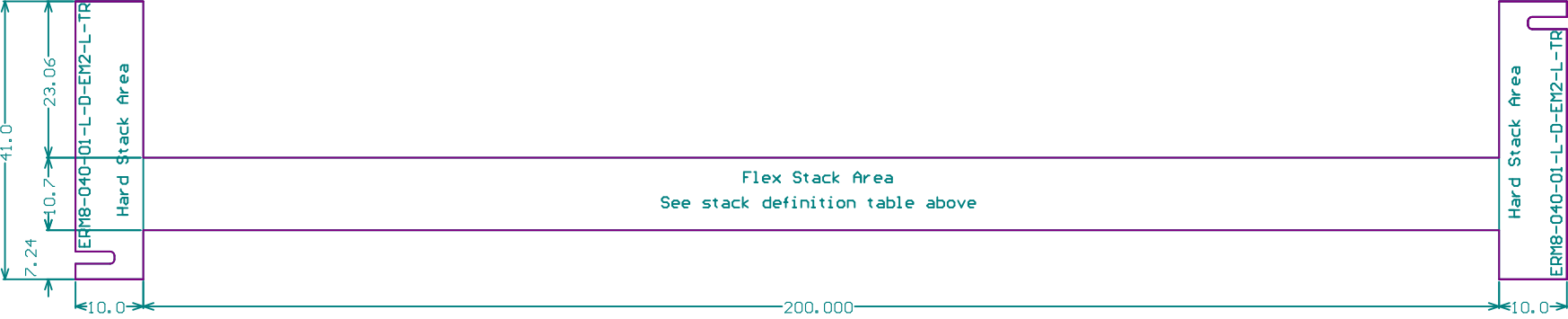
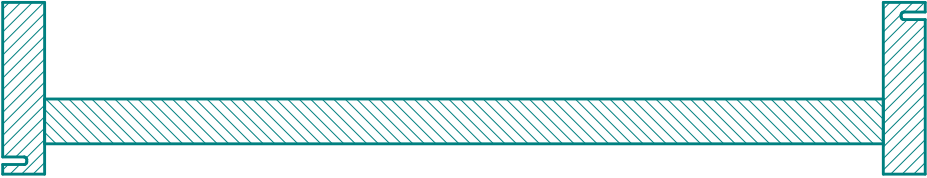




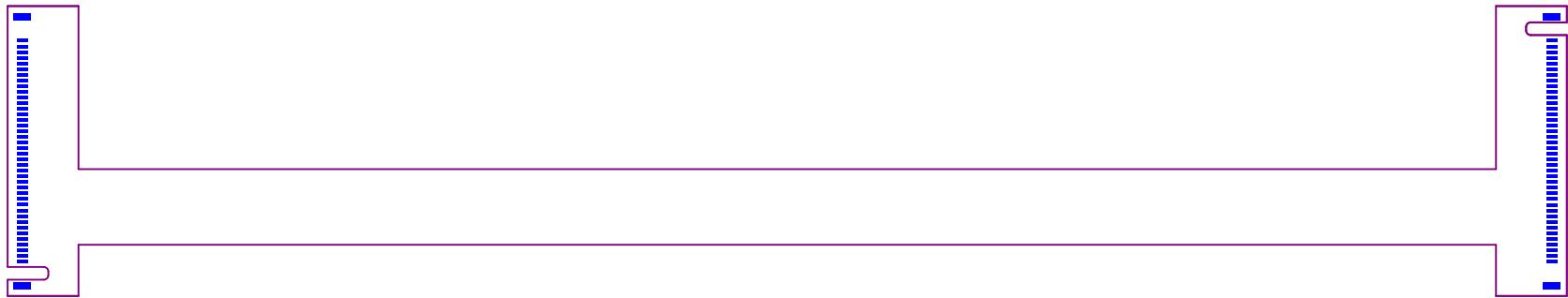




Layer	Name	Material	Thickness	Constant	Hard Stack	Flex Stack
1	Top Overlay					
2	Top Solder	Solder Resist	0.010mm	3.5		
3	L1	Copper	0.035mm			
4	Dielectric 1	FR-4	0.650mm	4.2		
5	L2	Copper	0.035mm			
6	Dielectric 2	Flex	0.150mm	4.2		
7	L3	Copper	0.035mm			
8	Dielectric 3	FR-4	0.650mm	4.2		
9	L4	Copper	0.035mm			
10	Bottom Solder	Solder Resist	0.010mm	3.5		
11	Bottom Overlay					







Symbol	Hit Count	Finished Hole Size	Plated	Hole Type
☆	4	78.74mil (2.000mm)	PTH	Round
□	52	7.87mil (0.200mm)	PTH	Round
	56 Total			





Comment	Description	Designator	Footprint	LibRef	Quantity
ERM8-040-01-L-D-EM2-L		P1, P2	ERM8-040-01-L-D-EM2-L	ERM8-040-01-L-D-EM2-L	2